Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("20040268054").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/12 15:39
L2	1	((controller or (control$4 near3 (logic device circuit circuitry))) with cach$3) and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/12 15:42
L3	2	(re-validat$3 revalidat$3) same (history with (table list register buffer queue directory memory storage)) same (cach$3 with (line entry address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/12 15:44
L4	2	((re-validat$3 revalidat$3) same (history with (table list register buffer queue directory memory storage)) same (cach$3 with (line entry address))).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/12 15:47
L5	2	((dirty with invalid$3) same (clean with invalid$3)) same (history near5 (table list buffer queue register directory memory storage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/12 15:49
L6	12	((re-validat$3 revalidat$3) with (cach$3 with (entry address line))) and (history with (table list register buffer memory storage directory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/12 15:54